



IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Padmanabha I. Venkitakrishnan

Confirmation No.:

Application No.: 09/920,382

Examiner: Unknown

Filing Date: Jul 31, 2001

Group Art Unit:

Title: Method For Derving A Benchmark Program For Estimating The Maximum Power Consumed In A Microprocessor

COMMISSIONER FOR PATENTS  
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97(b), or  
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)
- ☐ under 37 CFR 1.97 (c) together with either a:  
☐ Statement under 37 CFR 1.97(e), or  
☐ a \$180.00 fee under 37 CFR 1.17(p), or  
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ under 37 CFR 1.97 (d) together with a:  
☐ Statement under 37 CFR 1.97(e), and  
☐ a petition under 37 CFR 1.97(d)(2), and  
☐ a \$180.00 petition fee set forth in 37 CFR 1.17(p).  
(Filed after final action or notice of allowance, whichever occurs first, but before payment of the issue fee)

Please charge to Deposit Account 08-2025 the sum of \$0.00. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account 08-2025 pursuant to 37 CFR 1.25.

☒ Applicant(s) submit herewith Form PTO 1449 - Information Disclosure Citation together with copies, of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

☐ A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individuals(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

It is requested that the information disclosed herein be made of record in this application.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231.

Date of Deposit: 08/21/2001

Typed Name: Ann Jesus

Signature: 

Respectfully submitted,

Padmanabha I. Venkitakrishnan

By 

Lloyd E. Dakin, Jr.

Attorney/Agent for Applicant(s)

Reg. No. 38,423

Date: 08/21/2001



FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANT'S INFORMATION DISCLOSURE  
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

10008005-1

APPLICATION NO.

09/920,382

CONFIRMATION NO.

APPLICANT

Padmanabha Venkitakrishnan

FILING DATE

07/31/2001

GROUP

## REFERENCE DESIGNATION

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
	1A					
	1B					
	1C					
	1D					
	1E					
	1F					
	1G					
	1H					
	1I					
	1J					
	1K					

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	TRANSLATION	
							YES	NO
	1L							
	1M							
	1N							
	1O							
	1P							

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

1Q	Dake Liu and Christer Svensson - "Power Consumption Estimation in CMOS VLSI Chips" - June 1994- pages 663-670
1R	Rita Yu Chen, Mary Jane Irwin and Raminder S. Bajwa - "An Architectural Level Power Estimator" June 1998 - pages 1-5
1S	

EXAMINER

DATE CONSIDERED